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EXAMINER	
NORRIS, JEREMY C	
ART UNIT	PAPER NUMBER
2841	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/18/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/735,668	MORAN, DOV	
	Examiner	Art Unit	
	Jeremy C. Norris	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *18 October 2006*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1,5,7,9-11 and 13-23* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☒ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1,5,7 and 13-23* is/are rejected.

7)☒ Claim(s) *9-11* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *16 December 2003* is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Double Patenting

Applicant is advised that should claim 15 be found allowable, claim 23 will be objected to under 37 CFR 1.75 as being a substantial duplicate thereof. When two claims in an application are duplicates or else are so close in content that they both cover the same thing, despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 15, 17, 19, and 21-23 are rejected under 35 U.S.C. 102(b) as being anticipated by US 6,097,086 (Crane).

The Examiner notes that it has been held that the recitation that an element is "for" performing a function is not a positive limitation but only requires the ability to so perform. It does not constitute a limitation in any patentable sense. *In re Hutchison,* 69 USPQ 138.

Crane discloses, referring primarily to figure 78, an electronic module, comprising; electronic circuitry (11); first connection mechanism (17), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed

circuit board by a first method; and a second connection mechanism (40), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method, wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 36, lines 15-30) [**claim 1**], wherein one of said connection mechanisms (17) is for mounting the electronic module on a printed circuit board by plugging said electronic module into said printed circuit board [**claim 22**].

Similarly, Crane discloses, an electronic module, comprising: (a) electronic circuitry (11); (b) a first electrical connection mechanism (17), directly operationally connected to said electronic circuitry, for mounting of the electronic module by a first method; and (c) a second electrical connection mechanism (40), directly operationally connected to said electronic circuitry, for mounting of the electronic module by a second method different from said first method; wherein mounting using only one of said connection mechanisms suffices to render the electronic module fully operational (col. 36, lines 15-30) [**claim 15**].

Additionally, Crane discloses, an electronic module, comprising: (a) electronic circuitry (11); (b) a first connection mechanism (40), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by robotic mounting; and (c) a second connection mechanism (17), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed

circuit board by manual mounting; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational [**claim 17**].

Furthermore, Crane discloses, an electronic module, comprising; (a) electronic circuitry (11); (b) a first connection mechanism (17), directly operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (40), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 36, lines 15-30) [**claim 18**].

Moreover, Crane discloses, an electronic module, comprising: (a) electronic circuitry (11); (b) a first connection mechanism (17), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (40), directly operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism

suffices to render the electronic module fully operational (col. 36, lines 15-30) [**claim 19**].

Also, Crane discloses, an electronic module, comprising: (a) electronic circuitry (11); (b) a first connection mechanism (17), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (40), including at least one electrically conducting pad (not specifically referenced, but shown at the end of leads 40), and operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational [**claim 21**].

Similarly, Crane discloses, an electronic module, comprising: (a) electronic circuitry (11); (b) a first connection mechanism (17), directly operationally connected to said electronic circuitry, for mounting of the electronic module by a first method; and (c) a second connection mechanism (40), directly operationally connected to said electronic circuitry, for mounting of the electronic module by a second method different from said first method; wherein mounting using only one of said connection mechanisms suffices to render the electronic module fully operational (col. 36, lines 15-30) [**claim 23**].

Claims 1, 5, 7, 13, 14, 16, 18-20, and 22 are rejected under 35 U.S.C. 102(b) as being anticipated by US 4,996,587 (Hinrichsmeyer).

Hinrichsmeyer discloses, referring primarily to figures 5 & 6, an electronic module, comprising; electronic circuitry (19); first connection mechanism (24), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and a second connection mechanism (37), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method, wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 2, line 40 – col. 3, line 25) [**claim 1**], further comprising: (d) an electrically insulating body (10) whereon said electronic circuitry, said first connection mechanism and said second connection mechanism are mounted [**claim 13**], wherein both of said first connection mechanism and said second connection mechanism are mounted on a common side of said body (as shown in the figures one of the leads 24 and a corresponding plug 37 are both mounted on the right side of the body 10) [**claim 14**] wherein one of said connection mechanisms (37) is for mounting the electronic module on a printed circuit board by plugging said electronic module into said printed circuit board [**claim 22**].

Similarly, Hinrichsmeyer discloses, an electronic module, comprising: (a) electronic circuitry (19); (b) a first electrical connection mechanism (24), operationally

connected to said electronic circuitry, for mounting of the electronic module by a first method; (c) a second connection mechanism (37), operationally connected to said electronic circuitry, for mounting of the electronic module by a second method different from said first method; and (d) an electrically insulating body (10) whereon said electronic circuitry, said first connection mechanism and said second connection mechanism are mounted; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 2, line 40 – col. 3, line 25) [**claim 16**].

Furthermore, Hinrichsmeyer discloses, an electronic module, comprising; (a) electronic circuitry (19); (b) a first connection mechanism (24), directly operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (37), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 2, line 40 – col. 3, line 25) [**claim 18**], wherein said second connection mechanism is operationally connected to said electronic circuitry via said first connection mechanism [**claim 5**].

Alternately, Hinrichsmeyer discloses, an electronic module, comprising: (a) electronic circuitry (19); (b) a first connection mechanism (37), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (24), directly operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational (col. 2, line 40 – col. 3, line 25) [**claim 19**], wherein said first connection mechanism is operationally connected to said electronic circuitry via said second connection mechanism [**claim 7**].

Moreover, Hinrichsmeyer discloses, an electronic module, comprising: (a) electronic circuitry (19); (b) a first connection mechanism (24), including at least one substantially hemispherical solder ball (25), and operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a first method; and (c) a second connection mechanism (37), operationally connected to said electronic circuitry, for mounting of the electronic module on a printed circuit board by a second method different from said first method; wherein mounting using only said first connection mechanism suffices to render the electronic module fully operational; and wherein mounting using only said second connection mechanism suffices to render the electronic module fully operational [**claim 20**].

Allowable Subject Matter

Claims 9-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: Claim 9 states the limitation "wherein said second connection mechanism includes at least one electrically conducting pad". This limitation, in conjunction with the other claimed features, was neither found to be disclosed in, nor suggested by the prior art.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is 571-272-1932. The examiner can normally be reached on Monday - Friday, 9:30 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard can be reached on 571-272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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1/8/07